

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L1 | 6 | remap\$4 with logic with resource | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:10 |
| L2 | 184 | remap\$4 with resource | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:10 |
| L3 | 24 | (remap\$4 with resource) and (fpga) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:11 |
| L4 | 20 | (remap\$4 with resource) and (soc or (system near3 chip)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:17 |
| L5 | 3083 | (remap\$4 or reconfigur\$4) same resource | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:18 |
| L6 | 674 | (remap\$4 or reconfigur\$4) same resource and (fpga) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:18 |
| L7 | 197 | (remap\$4 or reconfigur\$4) same resource and (fpga) and (soc or system near3 chip) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:18 |
| L8 | 32 | 7 and "716"/\$.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:53 |

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|-----|--------|---|--|----|----|------------------|
| L9 | 3464 | (fpga or soc or system near3 chip) and ((available or unused or utilized) with resources) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:55 |
| L10 | 346 | (fpga or soc or system near3 chip) and ((available or unused or utilized) with resources) and (remap\$4 or reallocat\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 08:59 |
| L11 | 345783 | (fpga or soc or system near3 chip or asic or asip) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 09:00 |
| L12 | 40 | (fpga or soc or system near3 chip or asic or asip) same remap | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 09:00 |
| L13 | 120 | (fpga or soc or system near3 chip or asic or asip) same remap\$4 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 09:00 |
| L14 | 39374 | (fpga or soc or system near3 chip or asic or asip) same (remap\$4 or reconfigur\$4 or configur\$4 or reallocat\$4 or substitut\$4 or replac\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 09:01 |
| L15 | 8960 | (fpga or soc or system near3 chip or asic or asip) same (remap\$4 or reconfigur\$4 or configur\$4 or reallocat\$4 or substitut\$4 or replac\$4) same (component or block or element or macro) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 09:02 |
| L16 | 4156 | (fpga or soc or system near3 chip or asic or asip) with (remap\$4 or reconfigur\$4 or configur\$4 or reallocat\$4 or substitut\$4 or replac\$4) same (component or block or element or macro) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 09:02 |

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|-----|------|---|--|----|----|------------------|
| L17 | 2819 | (fpga or soc or system near3 chip or asic or asip) with (remap\$4 or reconfigur\$4 or configur\$4 or reallocat\$4 or substitut\$4 or replac\$4) with (component or block or element or macro) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 09:02 |
| L18 | 306 | (fpga or soc or system near3 chip or asic or asip) with (remap\$4 or reconfigur\$4 or configur\$4 or reallocat\$4 or substitut\$4 or replac\$4) with (component or block or element or macro) and "716"/\$.ccls | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/21 10:03 |